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Title of Invention	STRUCTURE AND METHOD FOR LOCAL RESISTOR ELEMENT IN INTEGRATED CIRCUIT TECHNOLOGY	
Application Number :		
Date :		
First Named Applicant:	Jason P. Gill	
Confirmation Number:		
Attorney Docket Number:	BUR920030118US1	
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Submitted By:		Elec. Sign.
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Documents being submitted:	Files
us-assignment	BUR920030118US1-usassn.xml us-assignment.xsl us-assignment.dtd BUR920030118US1Assign1.tif BUR920030118US1Assign2.tif BUR920030118US1Assign3.tif
us-request	BUR920030118US1-usrequ.xml us-request.dtd us-request.xsl
us-fee-sheet	BUR920030118US1-usfees.xml us-fee-sheet.xsl us-fee-sheet.dtd
us-ids	BUR920030118US1-usidst.xml us-ids.dtd us-ids.xsl
application-body	BUR920030118US1spec-trans.xml us-application-body.xsl application-body.dtd wipo.ent mathml2.dtd mathml2-qname-1.mod isoamsa.ent isoamsb.ent isoamsc.ent isoamsn.ent isoamso.ent isoamsr.ent isogr3.ent isomfrk.ent isomopf.ent isomscr.ent isotech.ent isobox.ent isocyr1.ent isocyr2.ent isodia.ent isolat1.ent isolat2.ent isonum.ent isopub.ent mmlextra.ent mmlalias.ent soextblx.dtd BUR920030118US1FIGSsheet1of12.tif BUR920030118US1FIGSsheet2of12.tif BUR920030118US1FIGSsheet3of12.tif BUR920030118US1FIGSsheet4of12.tif BUR920030118US1FIGSsheet5of12.tif BUR920030118US1FIGSsheet6of12.tif

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us-declaration	BUR920030118US1Dec1.tif
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us-declaration	BUR920030118US1Dec4.tif
Comments	
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